



# VLSI-SoC 2024: Call for Papers

October 6-9, 2024

Tanger, Morocco

<https://vlsisoc2024.nl/>

**Paper Submission:** Papers should present original research and industrial results not published or submitted for publication in other forums. Electronic submission in PDF format to the Molesystems platform for VLSI-SoC 2024 is required. The proceedings will be published by IEEE and will be made available through IEEE Xplore. A selection of the conference best papers will be invited to submit an extended version to be included as chapters of a book to be published by Springer.

**Paper Format:** Papers should not exceed 6 pages (single-spaced, 2 columns, 10pt font). Submissions should be camera-ready, following the IEEE proceedings specifications.

**Paper Publication and Presenter Registration:** Papers will be accepted for regular or poster presentation at the conference. Every accepted paper **MUST** have at least one author registered at the conference by the time the camera-ready paper is submitted; the registered author is also expected to attend the conference and present the paper. A limited number of travel grants are available to needy PhD students from emerging countries.

**PhD Forum and PhD School:** A PhD Forum with a dedicated poster session will be included in the technical program, and PhD school on design and engineering of electronics systems will be co-located with the main conference.

VLSI-SoC 2024 is the 32th in a series of international conferences sponsored by IFIP WG 10.5, IEEE CEDA, and IEEE CASS, which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems. VLSI-SoC'24 will take place at hotel Barcelo, Tanger, Morocco, and it is organized by the Delft University of Technology.

VLSI-SoC 2024 will be held under the theme "SoCs for 5G Evolution and 6G" exploring the design and optimization challenges around 5G+6G communication systems and devices embedded systems, security, low power circuits, powered by CMOS and beyond CMOS-technologies.



The areas of interest include (but are not limited to) the following topics:

- **Analog design:** modelling, simulation, verification, and prototyping of analog, mixed-signal, RF, sensors, and IoT circuits
- **Digital design:** modelling, simulation, verification, and prototyping of circuits and systems, SoC, NoC, reconfigurable, IoT, and low-power architectures
- **Design for AI hardware and emerging applications:** unconventional computing, brain inspired computing, computation-in-memory, photonics, quantum, etc.
- **EDA tools and methodologies for IC design**
- **Hardware Dependability:** Design for testability, reliability, fault tolerance, security, safety, and variability
- **Other specialized topics;** e.g., Signal processing, Communications/ 5G/ 6G; Biomedical, MEMS, Cyberphysical systems, Power electronic devices and circuits, etc.

General Chairs: Said Hamdioui, Delft University of Technology (NL) & Anass El Haddadi, ENSA (MA)

Program Chairs: Hussam Amrouch, Technical University of Munich (DE) & Ioana Vatejelu, TIMA Grenoble (FR)

Tutorial Chair: Leticia M.B. Pöhls, RWTH Aachen University (DE)

Special Session Chairs: Baker Mohammad, Khalifa University (AE) & Saraju Mohanty, University of North Texas (US)

Panel Chairs: Hassen Aziza, Aix-Marseille Université & Annachiara Ruospo, Politecnico di Torino (IT)

Awards Chairs: Fatih Ugurdag, Özyeğin University (TR) & Tiziana Margaria, University of Limerick (IE)

PHD Forum Chair: Florenc Demrozi, University of Stavanger (NO)

Publication Chairs: Moritz Fieback, Delft University of Technology (NL) & Anteneh Gebregiorgis, Delft University of Technology (NL)

Finance Chairs: Motta Taouil, Delft University of Technology (NL) & Carlo Galuzzi, Delft University of Technology (NL)

Web Chair: Heba Abunahla, Delft University of Technology (NL) & Mark van Beusekom, Delft University of Technology (NL)

Sponsoring Chairs: Mohamed Bouhorma, Abdelmalek Essaâdi University (AE)

Local Arrangement Chairs: Nabila Harboul, Future Professionals Academy (NL) & Jaber El Bouhdidi, ENSA (MA)



Abstract Submission	3 May 2024
Final Paper Submission	10 May 2024
Author Notification	10 June 2024
Camera Ready	30 June 2024