VLSI-SoC 2024: Special Sessions

October 6-9, 2024
Tanger, Morocco
https://vlsisoc2024.nl/

Call for Special Sessions: VLSI-SoC'24 seeks proposals for Special Sessions. Proposed topics include, but are not limited to, hot and emerging topics relevant to VLSI-SoC, as well as cross topics (i.e. topics which can be considered "border line" for the VLSI community, but have, or are expected to have, a significant impact on test research/development/application/implementation activities). Proposers of Special Sessions are assumed to be able and willing to serve as organizers of the session, i.e., contact speakers, moderators, etc. Special Session organizers can also be speakers or moderators in their own proposals. Perspective Special Session organizers can also collaborate with potential embedded tutorial presenters, for proposing a special track topic which can homogenize a special session and a tutorial session across a single topic.

Guideline: Submitted proposals must satisfy an abstract of up to two A4 pages outlining the session's scope and topic(s), the objectives and relevance of the proposed session to VLSI-SoC, the format of the session, a list of prospective speakers along with short bios for each potential speaker. Your proposal should indicate the duration and layout of your proposed session. The duration should be at least 60 minutes AND no more than 90-minutes. The layout should show at least two 30-minutes (long) presentations AND at most four presentations.
For detailed instructions see:
https://vlsisoc2024.nl/index.php/calls/special-sessions

Publication: Upon acceptance, special session organizers will collaborate with the session speakers to summarize the contributions in 2 to 4 papers, one paper per presentation; each paper should be 6 page in length. The contributors have also the option to prepare a combined single paper with up to 10 pages, instead of 6-page separate papers. The papers associated with accepted special sessions will be included in the formal proceedings. The papers will undergo a fast review process by the SS co-chairs and a dedicated committee, in coordination with the Special Sessions co-Chairs. Each presenter has to register.

Contact Information: For more information, please contact the Special Session Chairs.

VLSI-SoC 2024 is the 32th in a series of international conferences sponsored by IFIP WG 10.5, IEEE CEDA, and IEEE CASS, which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems. VLSI-SoC'24 will take place at hotel Barcelo, Tanger, Morocco, and it is organized by the Delft University of Technology.

VLSI-SoC 2024 will be held under the theme "SoCs for 5G Evolution and 6G" exploring the design and optimization challenges around 5G+6G communication systems and devices embedded systems, security, low power circuits, powered by CMOS and beyond CMOS-technologies.

The areas of interest include (but are not limited to) the following topics:
- **Analog design:** modelling, simulation, verification, and prototyping of analog, mixed-signal, RF, sensors, and IoT circuits
- **Digital design:** modelling, simulation, verification, and prototyping of circuits and systems, SoC, NoC, reconfigurable, IoT, and low-power architectures
- **Design for AI hardware and emerging applications:** unconventional computing, brain inspired computing, computation-in-memory, photonics, quantum, etc.
- **EDA tools and methodologies for IC design**
- **Hardware Dependability:** Design for testability, reliability, fault tolerance, security, safety, and variability
- **Other specialized topics;** e.g., Signal processing, Communications/ 5G/ 6G; Biomedical, MEMS, Cyberphysical systems, Power electronic devices and circuits, etc.

General Chairs: Said Hamdioui, Delft University of Technology (NL) & Anass El Haddadi, ENSA (MA)
Program Chairs: Hussam Amrouch, Technical University of Munich (DE) & Ioana Vatejelu, TIMA Grenoble (FR)
Tutorial Chair: Leticia M.B. Pöhls, RWTH Aachen University (DE)
Special Session Chairs: Baker Mohammad, Khalifa University (AE) & Saraju Mohanty, University of North Texas (US)
Panel Chairs: Hassen Aziza, Aix-Marseille Université & Annachiara Ruospo, Politecnico di Torino (IT)
Awards Chairs: Fatih Ugurdag, Özyeğin University (TR) & Tiziana Margaria, University of Limerick (IE)
PHD Forum Chair: Florenc Demrozi, University of Stavanger (NO)
Publication Chairs: Moritz Fieback, Delft University of Technology (NL) & Anteneh Gebregiorgis, Delft University of Technology (NL)
Finance Chairs: Motta Taouil, Delft University of Technology (NL) & Carlo Galuzzi, Delft University of Technology (NL)
Web Chair: Heba Abunahla, Delft University of Technology (NL) & Mark van Beusekom, Delft University of Technology (NL)
Sponsoring Chairs: Mohamed Bouhorma, Abdelmalek Essaâdi University (AE)
Local Arrangement Chairs: Nabila Harboul, Future Professionals Academy (NL) & Jaber El Bouhdidi, ENSA (MA)

Submission	20 May 2024
Author Notification	10 June 2024
Camera Ready	30 June 2024